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LIST OF PRIOR ART CITED
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APPLICANT Capps, Jr. et al.

Technology Center 2100

FILING DATE June 14, 2001

GROUP ART UNIT 2184

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
W	AA 6009476	Dec. 28, 1999	Flory et al.	709/301	Nov. 21, 1995
W	AB 6134676	Oct. 17, 2000	VanHuben et al.	714/39	Apr. 30, 1998
W	AC 5890014	Mar. 30, 1999	Long	395/828	Aug. 5, 1996
W	AD 6108734	Aug. 22, 2000	Shand	710/105	Dec. 1, 1997
W	AE 6088753	Jun. 11, 2000	Sheafor et al.	710/128	Aug. 2, 1999
W	AF 5848266	Dec. 8, 1998	Scheurich	395/558	Jun. 20, 1996
W	AG 5951664	Sep. 14, 1999	Lambrech et al.	710/124	Sep. 10, 1997
W	AH 5751978	May 12, 1998	Tipple	395/309	Nov. 13, 1995

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.		PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
W	AI	JP5089032A	Apr. 9, 1993	Japan	G06F 13/36		X
W	AJ	JP57111621A	Jul. 12, 1982	Japan	G06F 3/00		X
W	AK	JP60007553A	Jan. 16, 1985	Japan	G06F 11/30		X
W	AL	JP10143460A	May 29, 1998	Japan	G06F 13/10		X
W	AM	DE19740514C1	Dec. 24, 1998	Germany	G06F 11/34		X

OTHER PRIOR ART (including author, title, date, pertinent page, etc.)

W	AN	Foster, "Spreadsheet Approach for Early Optimization of Large Bus Switching Performance", IEEE Southern Tier Tech Conf Issue Cat. No. 90TH0313-7/New York, 1990, pp. 165-171, 281.
W	AO	Marshall, "Building Standards Based COTS Multiprocessor Computer Systems for Space Around a High Speed Serial Bus Network", 17th DASC. AIAA/IEEE/SAE. Digital Avionics Systems Conference. Proceedings (Cat. No. 98CH36267), Pt. vol. 1, pp. D29/1-D29/8 vol. 1, Published New York, NY, USA, 1998 2 vol. (xi+xv+1444) pp., Abstract.
W	AP	Bennett, "Break the Performance Bottlenecks in Today's Multiprocessor Designs", EDN (European Edition), vol. 39, no. 14, pp. 113-114, 117-118, 120, 7 July 1994, Abstract.
W	AQ	Pleshek, "Intelligent Host Bus Adapters Increase SCSI-2 Bus Performance", I&CS, vol. 65, no. 3, pp. 41-43, March 1992, Abstract
W	AR	Frank, "The Sbus: Sun's High Performance System Bus for RISC Workstations", COMPCON Spring '90: Thirty-Fifth IEEE Computer Society International Conference. Intellectual Leverage. Digest of Papers. (Cat. No. 90CH2843-1, pp. 189-194, Published Los Alamitos, CA, USA, 1990, Sponsored by IEEE, Abstract.

DATE CONSIDERED 12-08-03.

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.